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APPLICANTS

Hiroshi Shimizu, Kawasaki, JAPAN;

** CONTINUING DATA *****

None

** FOREIGN APPLICATIONS *****

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Yes *R*

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>				

ADDRESS

004372
ARENT FOX KINTNER PLOTKIN & KAHN
1050 CONNECTICUT AVENUE, N.W.
SUITE 400
WASHINGTON, DC
20036

TITLE

SEMICONDUCTOR MEMORY HAVING HIERARCHICAL BIT LINE STRUCTURE

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